AKIMOTO et al., SN 09/975,934RCE Amdt. dated 3 August 2004 Reply to OA dated 3 May 2004

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IN THE ABSTRACT:

ABSTRACT OF THE DISCLOSURE

An image display fer displaying displays image data on an image display part constructed by a display pixel array is disclosed. In an image display in which image data input means for inputting image data so that the display pixel array has two neighboring areas having different frame rates (> 0) is provided or image data is displayed on an image display part constructed by a display pixel array, there is provided image data input means which can input at least one moving image data and at least one still image data into the image display part at different frame rates (> 0). A high precision image display can be realized hardly changing a display pixel rewriting speed. A moving image signal output circuit and a still image signal output circuit output image data to the display pixel array, and they are provided as circuit configurations independent of each other.